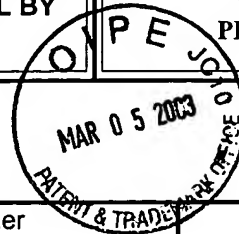


**SUBMISSION OF PROPOSED DRAWING AMENDMENT FOR APPROVAL BY
EXAMINER (37 CFR 1.121(a)(3)(ii) or 37 CFR 1.121(b)(3)(ii))**

Docket No.
PHUS010610



In Re Application Of: Letavic et al.

Serial No.
10/015,847

Filing Date
12/10/2001

Examiner
Lewis, M.

Art Unit
2822

Invention: **DUAL GATE OXIDE HIGH-VOLTAGE SEMICONDUCTOR DEVICE (as Amended)**

Address to:
Assistant Commissioner for Patents
Washington, D.C. 20231

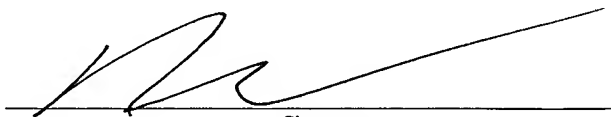
Attached please find:

(check applicable items)

- ☐ a sketch in permanent ink
- ☒ a copy of the original drawing(s) with red ink

showing proposed changes to the drawing(s) in this application for which the approval of the examiner is requested.

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Signature

Dated: February 28, 2003

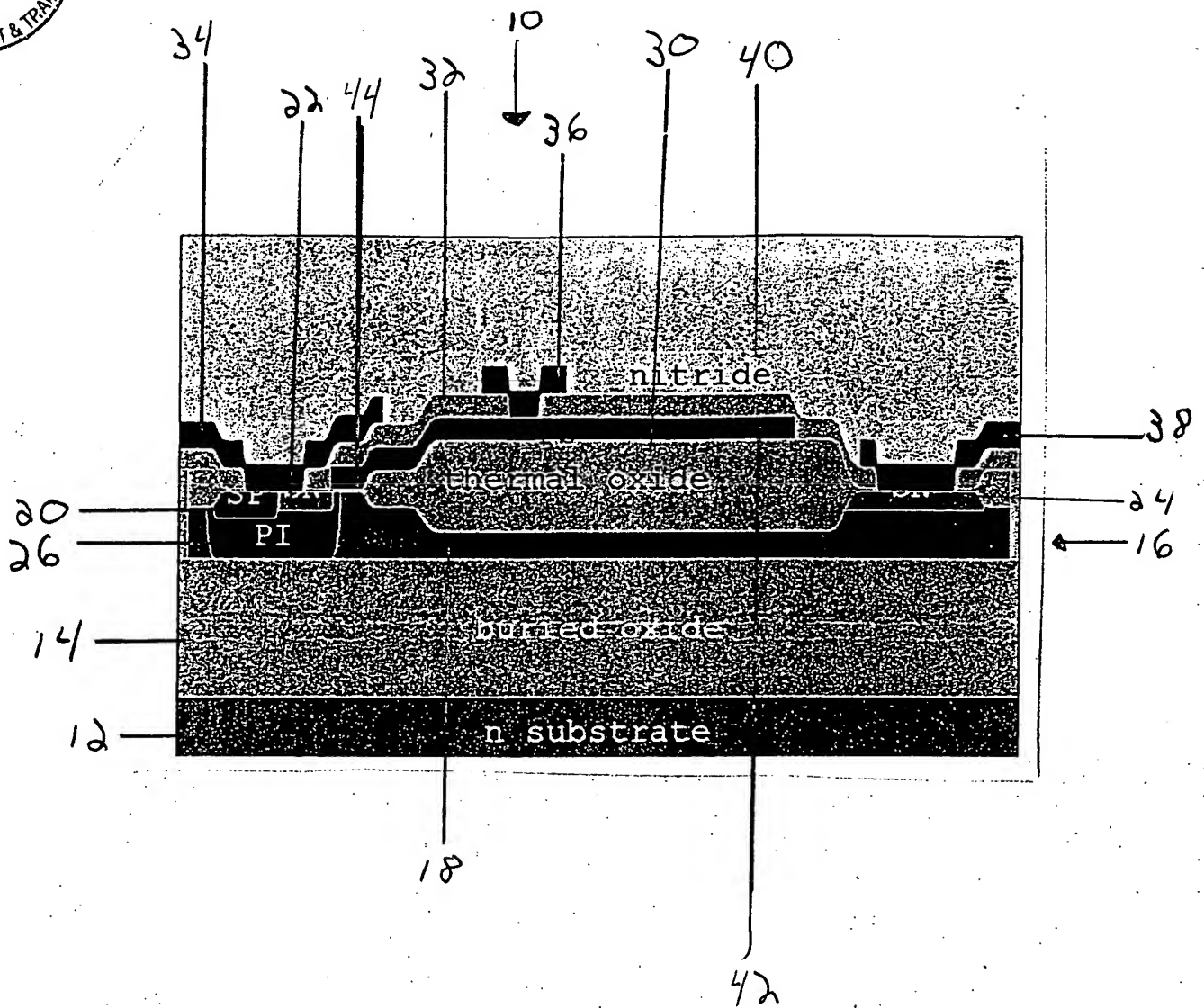
Ronald A. D'Alessandro, Reg. No. 42,456
Hoffman, Warnick & D'Alessandro LLC
Three E-Comm Square
Albany, NY 12207
(518) 449-0044 - Telephone
(518) 449-0047 - Facsimile

I certify that this document and fee is being deposited
on 2/28/2003 with the U.S. Postal Service as
first class mail under 37 C.F.R. 1.8 and is addressed to the
Assistant Commissioner for Patents, Washington, D.C.
20231.


Signature of Person Mailing Correspondence

Wendy E. Thompson

Typed or Printed Name of Person Mailing Correspondence



~~RELATED ART~~

PRIOR ART

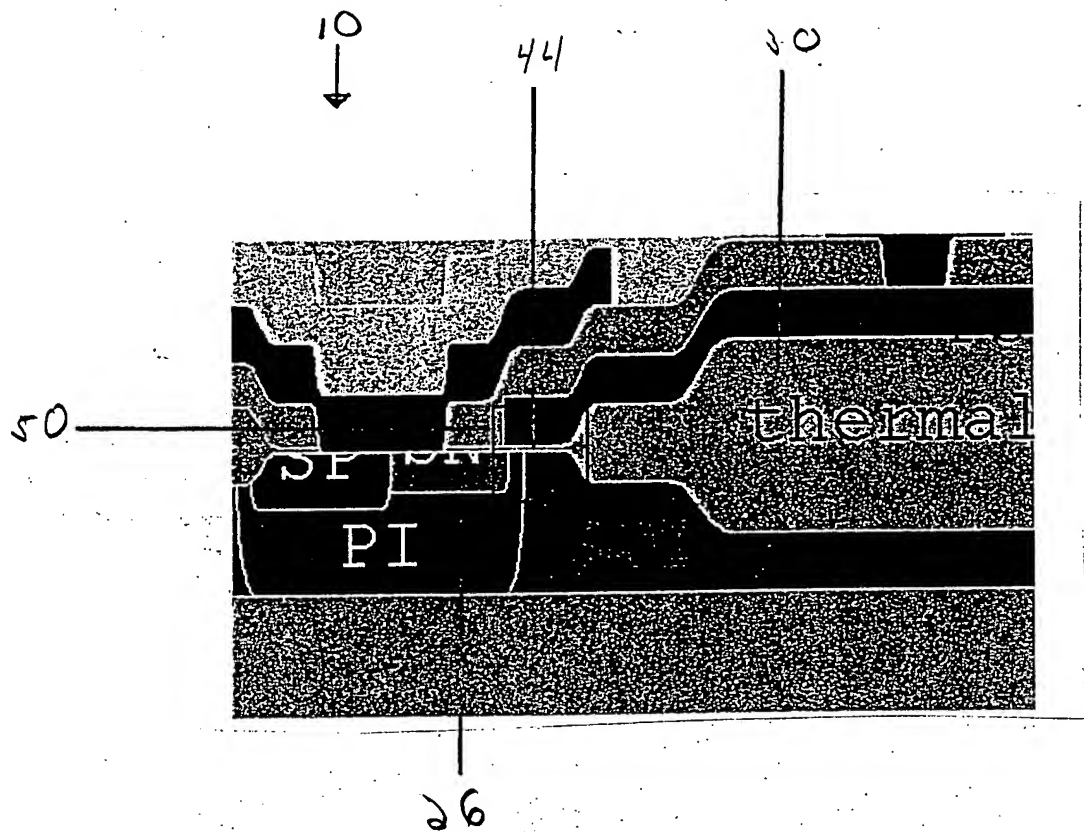
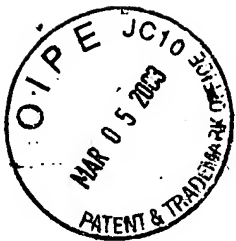


FIG. 2

~~RELATED ART~~

PRIOR ART